

## **REMARKS**

The Office Action dated March 27, 2006, has been received and carefully noted. The above amendments and the following remarks are being submitted as a full and complete response thereto. Reconsideration of this application in view of the foregoing amendments and the following remarks is respectfully requested.

By this amendment, Claims 1, 6, 12, 16, 19, and 20 have been amended. Claim 23 has been added. The Applicant submits that the claim amendments are fully supported in the specification as originally filed, for example, at paragraph [0083] of the specification. Accordingly, no new matter has been added. Claims 1-8, 12-20, and 23 are currently pending in the application and are subject to examination.

### **Rejection Under 35 U.S.C. § 102(b)**

Claims 1, 4-6, 8, 12, 14-17, 19, and 20 are rejected under 35 U.S.C. § 102(b) as being anticipated by Sha et al. (U.S. Patent No. 6,404,294, hereinafter "Sha"). To the extent this rejection remains applicable to the claims currently pending, the Applicant traverses the rejection.

Claim 1 recites a clock generator circuit comprising a phase comparator receiving a standard clock signal and an operating clock signal, and generating an output signal, and a voltage controlled oscillator generating the operating clock signal based on the output signal of the phase comparator, wherein the voltage controlled oscillator comprises a voltage current converter converting a voltage signal into a current signal, a current D/A converter fluctuating the current signal based on a digital signal, and a current controlled oscillator oscillating the operating clock signal of which frequency

corresponds to each of the variable current signals, wherein the current DA converter has a plurality of current circuits including a current circuit.

The Applicant submits that the applied prior art fails to teach or suggest all the elements of amended Claim 1. Specifically, Sha fails to teach or suggest at least a current DA converter **wherein the current DA converter has a plurality of current circuits including a current circuit.**

The Office Action cites element 110' of Sha as allegedly teaching a current DA converter, however, Sha fails to disclose a current DA converter having a plurality of current circuits, including a current circuit, as recited in the claimed invention. Accordingly, the Applicant submits that Sha fails to teach or suggest all the elements of amended Claim 1.

Similarly, Claims 6, 12, 17, 19, and 20 recite a current DA converter **wherein the current DA converter has a plurality of current circuits including a current circuit.** As discussed above with respect to Claim 1, Sha fails to teach or suggest at least this element of Claims 6, 12, 17, 19, and 20.

To qualify as prior art under 35 U.S.C. § 102(b), a single reference must teach, i.e., identically describe, each feature of a rejected claim. For the reasons provided above, the Applicant respectfully submits that Sha does not teach or suggest each and every feature recited by Claims 1, 6, 12, 17, 19, and 20. Accordingly, Claims 1, 6, 12, 17, 19, and 20 are not anticipated, nor rendered obvious in view of, Sha.

As such, the Applicant respectfully submits independent Claims 1, 6, 12, 17, 19, and 20 should be deemed allowable.

Claims 4, 5, 8, 15, and 16 depend either directly or indirectly from one of Claims 1, 6, and 12. As Claims 1, 6, and 12 are allowable, the Applicant submits that Claims 4, 5, 8, 15, and 16 are likewise allowable for at least the same reasons that Claims 1, 6, and 12 are allowable, as well as for the additional subject matter recited therein.

**Rejection Under 35 U.S.C. § 103(a)**

Claims 2, 3, 7, 13, and 18 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Sha in view of Applicant's Admitted Prior Art as represented by Fig. 1 (hereinafter "AAPA"). The Applicant respectfully traverses the rejections.

Claims 2 and 3 depend from Claim 1, Claim 7 depends from Claim 6, Claim 13 depends from Claim 12, and Claim 18 depends from Claim 17.

The Applicant respectfully notes that Sha in view of AAPA, alone or in any combination thereof, fail to teach or suggest at least a current DA converter **wherein the current DA converter has a plurality of current circuits including a current circuit**, as recited in Claims 1, 6, 12, and 18. In other words, AAPA fails to overcome or otherwise address the deficiencies of the applied prior art with regards to Claims 2, 3, 7, 13, and 18.

To establish *prima facie* obviousness of a claimed invention, all the claim limitations must be taught or suggested by the prior art. M.P.E.P. § 2143.03. For at least the reasons provided above, the Applicant submits that Sha in view of AAPA, alone or in any combination, do not teach or suggest all the elements of Claims 2, 3, 7, 13, and 18. Accordingly, the Applicant submits that Claims 2, 3, 7, 13, and 18 are allowable over the applied prior art.

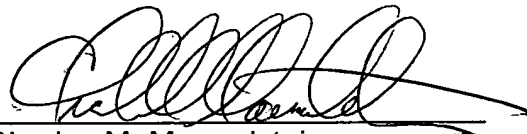
## **Conclusion**

In view of the foregoing, reconsideration of the application, withdrawal of the outstanding objections and rejections, allowance of Claims 1-8, 12-20, and 23, and the prompt issuance of a Notice of Allowability are respectfully solicited.

Should the Examiner believe anything further is desirable in order to place this application in better condition for allowance, the Examiner is requested to contact the undersigned at the telephone number listed below.

In the event that this paper is not being timely filed, the Applicant respectfully petitions for an appropriate extension of time. Any fees for such an extension, together with any additional fees that may be due with respect to this paper, may be charged to Counsel's Deposit Account Number 01-2300, referencing Docket Number 100698-00014.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Charles M. Marmelstein', written over a horizontal line.

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